

Name:

Student id:

Section: Serial#:

QUESTION ONE: Convert the following C++ code into equivalent assembly code.

{8 pts}

```
int AA[12]={2,-1,5,-6,9,-7,-8,0,4,-3,-2,9}
```

```
cout<< "The mystery numbers are: ";
```

```
for(j=0; j<12; j++)
```

```
{ if(AA[j] > 0)
```

```
cout<< AA[j]<< " ";
```

```
cout << endl;
```

• data

AA ~~word~~ sword

str1 byte

• code

clrscr

mov

call

mov

mov

cmp

jg

cmp

jle

mov

call

next?

call crlf

include iostream.h

data

m1 byte

AA sdword 2,-1,5,-6...

• code

Mov ecx,12

Mov esi,0

• The mystery numbers are: "

L1: Mov eax,AA[esi]

CMP eax,0

jg L2

INC esi

jmp L1

L2: call writeint

INC esi

jmp L1

Loop L2

Loop L2

call crlf

exit

return 0

end main

10101010

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QUESTION TWO: Write a sequence of assembly instructions to perform each of the following tasks:

- 1) Divide the signed values 47890CH / C000H {2 pt}

~~mov~~ ~~eax, 47890C H~~
~~mov~~ ~~ecx, C000 H~~
~~IDiv~~ ~~ecx~~

- 2) Set the odd-numbered bits in EBX register to 1. Leave other bits in EBX unchanged {2 pt}

~~pushfd~~ ~~eax~~
~~popfd~~ ~~ecx~~

~~mov~~ ~~ecx, 1~~
~~pushfd~~ ~~ecx~~
~~popfd~~ ~~ecx~~

- 3) Inverse the odd-numbered bits in ECX register. Leave other bits in ECX unchanged {2 pt}

~~XOR~~ ~~ecx, 1~~

~~XOR~~ ~~ecx, 00000001 H~~

- 4) Convert the signed value in AX into a double word in DX:AX {2 pt}

~~mov~~ ~~ax, val1~~
~~CWD~~

~~mov~~ ~~edx, val1~~

- 5) Set all bits in the flags register to 1. {2 pt}

~~and~~ ~~ecx, 0~~

~~push~~ ~~0FFFFFFFF H~~
~~popfd~~

- 6) Move the unsigned value in ax register into ebx register. {2 pt}

~~mov~~ ~~ebx, value~~
~~mov~~ ~~ebx, value~~

~~movzx~~ ~~ebx, ax~~

- 7) Store in EBX register the product of multiplying CL register by 1024. CL register may contain any unsigned value. (MUL and IMUL instructions are not allowed to be used). {2 pt}

~~mov~~ ~~ebx, 0~~
~~shl~~ ~~cl, 10~~
~~ax~~

~~movzx~~ ~~ebx, cl~~
~~shl~~ ~~ebx, 10~~

- 8) Swap the contents of 2 predefined memory words (M3, M7) without using MOV instructions. {2 pts}

~~pushfd~~ ~~eax, M3~~
~~pushfd~~ ~~eax, M7~~

~~popfd~~ ~~eax, M7~~
~~popfd~~ ~~eax, M3~~

di di ax
ax dx ax:dx
ecx ecx ecx:ecx

push m3
push m7
pop m3
pop m7

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QUESTION THREE:

{12 pts}

3. a) MOV AX, 3AE5H
TEST AX, 0FCD7H
OR AX, AX
AX = ~~FC7A~~ H 3AE5H

b) MOV AX, 4C9AH
MOV BX, 768BH
SHRD BX, AX, 3
BX = ~~BB5~~ H 9A76

c) MOV AX, 3A90H
MOV CX, 4CA0H
IMUL CL
AX = ~~17920~~ H 4600

d) MOV AX, 300BH
MOV BX, 2640H
DIV BL
AX = ~~192~~ H 0B C0

e) What will be in registers AX and ESP after executing the following instruction sequence?

MOV ESP, 3000H
MOV EAX, -1
MOV ECX, 2C67H
PUSH ECX
CMP AX, CX
JL L9
PUSH EAX
AND AX, 3A4CH
XOR AX, CX
L9:

ESP = 3000H
EAX = FFFFH
ECX = 2C67H
AX = FFFFH
CX = 2C67H
AX = 2C67H
CX = 2C67H

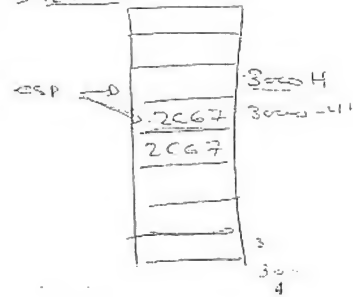
AX = ~~CE~~ H 3A4C
ESP = ~~3000~~ H 2FFC

f) If "JL L9" is replaced by "JB L9", what will be in registers AX, ESP after executing the above code?

AX = ~~2~~ H 2
ESP = ~~3000~~ H 2FF8

AX = FFFFH
ECX = 2C67H
AX = 2C67H
CX = 2C67H

AX = FFFFH
ECX = 2C67H



FFFFH
1400
1100

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QUESTION FOUR:

{8 pts }

Write a complete assembly program that: *unsign*

- Prompts the user to enter from the keyboard an integer value in the range 1-20.
- Reads the value and validates it against the above given range limits. If an invalid value is entered, loop until a valid value is entered.
- Calls a procedure TR18 that displays a triangle of stars "*" as shown in the example below
- The procedure accepts as a parameter the entered value in eax register.

For example, if you entered 5 from the keyboard, the output should be the following shape of stars:

```
*
**
***
****
*****
```

include Irvine32.inc

.data

integer byte ? *dup(20)*

TR18 byte "x", 10

.code

main proc

mov

mov

call

cmp

ja

mov

call

ja

next

call

exit

end

main

endp

main

offset integer
eax, integer
readint

eax, 20

next

eax, integer

readint

next

next

TR18

TR18 proc

mov

cmp

jmp

mov

next

ret

endp

TR18

next

ret

endp

TR18

next

ret

endp

TR18

next

ret

endp

TR18

next

ret

endp

TR18

next

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endp

TR18

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endp

TR18

next

ret

endp

TR18

next

ret

endp

TR18

next

ret

endp

TR18

next

ret

endp

TR18